# Harmonic elimination in multi level inverter by comparing various soft techniques

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*Abstract*-In this paper, soft computing techniques specially GA, PSO and HSA are applied in multilevel inverter to compute switching angles at fundamental frequency switching scheme by solving non linear transcendental equations (known as selective harmonic elimination equations), thereby keeping certain predominating lower order harmonics within allowable limits, and simultaneously, control over magnitude of output voltage of a multilevel inverter is achieved. Optimization capability of THD minimization, computational time, FFT analysis has been studied for these optimization techniques in MATLAB environment.

*Index Terms* -Multilevel Inverter; SHE-PWM; Particle Swarm Optimization; Genetic algorithm; Harmony Search algorithm (*Key words*).

# I. INTRODUCTION

Electrical energy is an extremely valuable commodity and many market studies demonstrate that the demand for electrical energy is continually increasing exponentially. In the period from 2001 to 2006, energy consumption increased by 16.1% increase [1]. In 2030, the energy consumption will be increased by 50% [2]. Owing to the limited availability of electrical energy and ever increasing oil prices, however, a new technical age has begun - an age in which the goal is to reduce electrical energy consumption and promote research into alternative sources of energy. As a result, continuous improvements are urgently needed on the efficiency front in all industrial and consumer applications. Inverters are very common application in power electronics. Many researches are going on to improve output voltage with minimum THD [23]. In response to the growing demand for high power inverter units, multilevel inverters have been attracting growing attention from academia as well as industry in the recent decade. Multilevel inversion is a power conversion strategy in which the output voltage is obtained in steps thus bringing the output closer to a sine wave and reduces the total harmonic distortion (THD) [23, 24]. Multilevel converters (MC) are the technology of choice for medium and high voltage application like flexible AC transmission systems (FACTS), laminators, mills, conveyors, compressors, UPS systems, broadcasting amplifier [3], plasma [4], industrial drive [5] as well as STATCOM [6] applications etc.

There are three main types of multilevel inverters: diode-clamped, capacitor clamped, and cascaded H-bridges [7]. Compared with the traditional two-level voltage inverter, the primary advantage of multilevel inverters is their smaller output voltage step, which results in high power quality, lower harmonic components, better electromagnetic compatibility, and lower switching losses [8]. There are several methods used for harmonic elimination in multilevel inverter. Widely used methods for harmonic elimination are sine-triangle PWM (SPWM), Optimal Minimization of Total Harmonic Distortion (OMTHD) and Selective Harmonic Elimination Pulse Width Modulation (SHE-PWM). Sine-triangle PWM method and its variants are very effective for controlling the inverter output voltage but this method is having very high switching frequency which causes switching loss [9].

The existing multilevel carrier-based pulse width modulation (PWM) strategies have no special provisions to offer quality output, besides lower order harmonics are introduced in the spectrum, especially at low switching frequencies [10]. In OMTHD, without any emphasis on special harmonics, all harmonics in the same weight (i.e. THD) are minimized [11, 12]. Selective harmonic elimination PWM (SHE-PWM) is a kind of switching control method benefited from its low switch frequency and high output power quality, and then saved the cost of filter for inverters [13].

There are some other methods like Newton-Raphson (N-R) method [14], Walsh functions [15], Blockpulse functions [16]. N-R methods have some drawbacks like divergence problems, require initial guess and gives no optimum solution. Solving linear equations, instead of nonlinear transcendental equations was done in Walsh function and Block-pulse function. Since the harmonic losses in a machine are determined by the ripple currents, a performance index related to undesirable effects of the harmonics should be defined to minimize instead of focusing on individual harmonics. The total energy of harmonics in a PWM waveform is constant; keeping the specific lower-order harmonics in allowable limits, minimizing other harmonics up to certain level will considerably boost the fundamental one.

In this paper, Particle Swarm optimization (PSO) [17], Genetic algorithm (GA) [18, 25] and Harmony Search algorithm (HSA) [19, 20] are used to minimize the overall THD of the output voltage of a 7 level Multilevel Cascaded H-Bridge Inverter with equal dc source. The objective function derived from the SHE problem is minimized, to compute the switching angles while lower order harmonics are controlled within allowable limits. This paper is organized as follows. The proposed scheme is described in Section 2. Simulation results and comparison are presented in Section 3 and finally conclusion in Section 4.

### II. PROPOSED SCHEME

A higher level ac voltage can be synthesized by cascading several lower level inverters supplied from equal or unequal dc sources. This configuration is known as multilevel configuration of inverters. A single-phase structure of a cascaded multilevel inverter is shown in Fig. 1. In case of equal dc sources  $Vdc1 = Vdc2 \dots = Vdcn = Vdc$ . The synthesized ac output voltage waveform is the sum of all the individual inverter outputs. The number of output phase voltage levels of cascade multilevel inverter is 2N + 1, where N is the number of dc sources. An output voltage waveform of a 7-level cascade multilevel inverter with three dc sources is shown in Fig. 2.

### III. CONVENTIONAL METDOD

The output voltage waveform V(t) of the multilevel inverter as shown in Fig. 2 can be represented by (1)

$$V(t) = \sum_{n=1}^{\infty} (a_n \sin n\alpha_n + b_n \cos n\alpha_n)$$
(1)

The even harmonics are absent (bn = 0) due to quarter wave symmetry of the output voltage [4]. The n-th harmonic an is expressed with the first quadrant switching angles  $\alpha_1, \alpha_2, \dots, \alpha_m$ .

an=	=(41	/dc/n	$\pi)\sum_{k}$	n=1	$\cos(n\alpha k)$	(2)	and
			_				

$$0 < \alpha 1 < \alpha 2 < \dots \alpha k < (\pi 2) \tag{3}$$

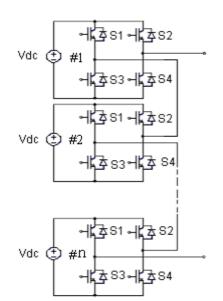


Fig.1 Single-phase configuration of a multilevel inverter

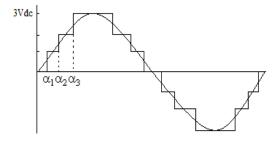


Fig.2 Output voltage waveform of a 7-level multilevel inverter

For any odd harmonics, (2) can be expanded up to the k-th term where m is the number of variables corresponding to switching angles  $\alpha 1$  through  $\alpha m$  of the first quadrant. In selected harmonic elimination, an is assigned the desired value for fundamental component and equated to zero for the harmonics to be eliminated [5].

$$a1 = 4(Vdc/\pi)\sum_{k=1}^{n} \cos(\alpha k) = M$$
$$a5 = 4(Vdc/5\pi)\sum_{k=1}^{n} \cos 5(\alpha k) = 0$$
$$an = 4(Vdc/n\pi)\sum_{k=n}^{n} \cos n(\alpha k) = 0$$

where M is the amplitude of the fundamental component.

Nonlinear transcendental equations are thus formed and after solving these equations,  $\alpha 1$  through  $\alpha k$  are computed. Triplen harmonics are eliminated in three-phase balanced system and these are not considered in (4). It is evident that (m-1) harmonics can be eliminated with m number of switching angles. These nonlinear equations show multiple solutions and the main difficulty is its discontinuity at certain points where no set of solution is available [14]. This limitation is addressed in the present method to ease the online application at these points of discontinuity.

### IV. PROPOSED METHOD

### 1. PSO Method

The PSO methodology is a very powerful tool for optimization of nonlinear functions. The method was discovered through simulation of a simplified social model viz. bird flocking, fish schooling, etc. [12] and presently being used in many applications for optimization of nonlinear equations. Physically, this mimics a flock of birds that communicate together as they fly. Each bird looks in a specific direction, and then when communicating together, they identify the bird that is in the best location. Accordingly, each bird speeds towards the best bird using a velocity that depends on its current position. Each bird, then, investigates the search space from its new local position, and the process repeats until the flock reaches a desired destination.

It is important to note that the process involves both social interaction and intelligence so that birds learn from their own experience (local search) and also from the experience of others around them (global search) [22].The conventional SHE technique for multilevel inverter has the disadvantage of complexity to solve the nonlinear transcendental equations that have multiple solutions [15, 16]. Moreover, at certain points, no solutions are available to satisfy these equations. In the proposed PSO method, the complexity of finding the solution of these nonlinear equations is avoided by converting the SHE problem to an optimization problem. The %THD of the output voltage can be computed using (5).

%*THD*=  $[(1/a_1^2)\sum_{n=5}^{\infty}(an)^2]^{1/2}*100$ Where  $n=6i\pm 1$  (i=1,2,3,...)

In the developed PSO algorithm, the same expression of the voltage THD is considered as the objective function ( $\alpha$ ) and minimized with the constraints of individual harmonics limits and minimal variations of switching angles. The formulation of the problem will be as follows: Minimize

$$(\alpha) = (\alpha_{1}, \alpha_{2}, \dots \alpha_{m}) \tag{6}$$

Subjected to:

$$0 < \alpha 1 < \alpha 2 < \dots \alpha k < \pi 2;$$

$$a1 = M$$

$$a5 = \varepsilon 1$$

$$a7 = \varepsilon 2$$

$$\vdots$$

$$an \le \varepsilon n \qquad (7)$$

where  $\varepsilon_1$ ,  $\varepsilon_2$ ,....  $\varepsilon_n$  are the allowable limits of individual harmonics. A flowchart of the PSO algorithm is shown in Fig. 3. With a considerable number of generations and large number of population in each generation, the algorithm searches for all probable set of solutions and finally compute the angles  $\alpha_1$  through  $\alpha_m$  to contribute either the lowest THD or next to the lowest one based on changes in the switching angles, keeping the individual harmonics within the limits as specified by (7). Also at the modulation indices of discontinuity, the switching angles al through am are computed based on possible minimum voltage THD optimizing the individual harmonics.

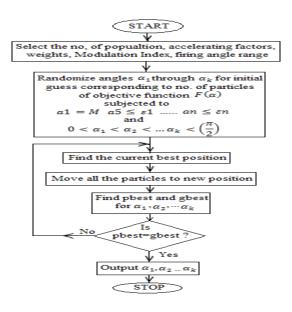


Fig.3 Flowchart of proposed PSO algorithm

### 2. GA METHOD

Genetic Algorithm (GA) is a method used for solving both constrained and unconstrained optimization problems based on natural selection, the process that drives biological evolution [9, 22]. The GA repeatedly modifies population of individual solutions. At each step, the GA selects individuals at random from the current population to be parents and uses them to produce the children for the next generation. Over successive generations, the population "evolves" towards an optimal solution. A flowchart of the Genetic algorithm is shown in Fig. 4.

GA uses three main rules at each step to create the next generation from the current population:

• *Selection rules* select the individuals, called *parents* that contribute to the population at the next generation.

• *Crossover rules* combine two parents to form children for the next generation.

• *Mutation rules* apply random changes to individual parents to form children.

# 3. HSA METHOD

Powerful optimization tool namely the music-inspired harmony based optimization algorithm. The algorithm is based on the observation that the aim of music creation is the quest of the perfect state of harmony. Such a quest is relevant to the optimization procedures which also seek the "perfect" solution [17-18]. Harmony Search Algorithm is based on the musical process of searching for the perfect state of harmony.

A flowchart of the Genetic algorithm is shown in Fig. 5. Harmony Search Algorithm (HSA) presents several advantages as the following:

- a) HAS imposes fewer mathematical requirements and does not require initial value settings for decision variable
- b) As the HAS uses stochastic random searches, derivative information is also unnecessary.
- c) The HSA generates a new vector, after considering all of the existing vectors, whereas the method like genetic algorithm (GA) only considers two parent vectors.
- d) HAS does not need to encode and decode the decision variables into binary strings.

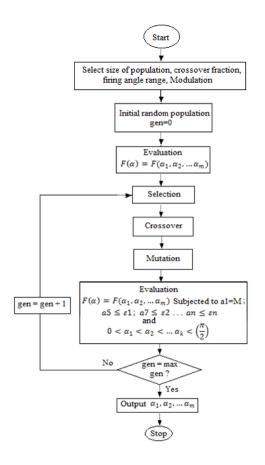


Fig.4 Flowchart of proposed Genetic algorithm

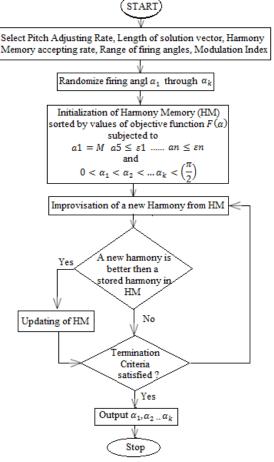


Fig.5 Flowchart of proposed Harmony Search Algorithm

# V. SIMULATION RESULTS AND DISCUSSION

The proposed scheme has been simulated in MATLAB/Simulink environment. For three dc sources, the multiple sets of angles present within the modulation index range of 0.20-1.10, eliminating harmonics are computed. The Switching angle against Modulation Index is shown in Fig 6, Fig 7 and Fig 8 using GA, PSO and HSA. The voltage THD against modulation index is shown in Figs. 9 using PSO, GA and HSA as an optimization tool. Harmonic Spectrum for output phase voltage at 1 Modulation Index is shown in Fig 10. For a 7 level inverter, the time taken for particular THD is shown in table I. In PSO algorithm the acceleration factor c1 and c2 is taken as 0.2 and 0.5 for 40 numbers of particles. In GA optimization tool double vector population type is taken for 20 population sizes with 0.8 crossover fraction of scattered function. In Harmony Search Algorithm Pitch adjusting rate is taken as 0.7, Harmony Memory is accepting rate

0.95, Length of solution vector is 20 and switching angle range in between 0 to 90 degree. GA takes 51 generation, PSO takes 51 iteration and HAS takes maximum 500 number of attempts. For 7-Level Multilevel Inverter the THD is being optimized up to 31st order.

Fig.8 Switching angles versus modulation index for 7-level multilevel inverter using HSA

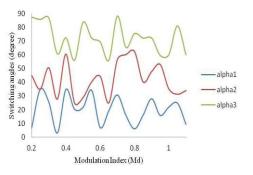


Fig.6 Switching angles versus modulation index for 7-level multilevel inverter using GA

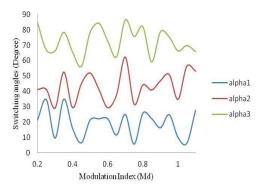


Fig.7 Switching angles versus modulation index for 7-level multilevel inverter using PSO

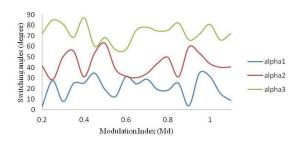


Table I. Comparison	between G	GA, PSO	and	HSA	technique	regarding
computational time at 0	.8 Modulat	tion Index	K			

Optimization technique	Computational technique	%THD
Genetic Algorithm	0.00490 seconds	4.2917
Particle Swarm Optimization	1.88086 seconds	5.7351
Harmony Search Algorithm	0.85458 seconds	5.3954

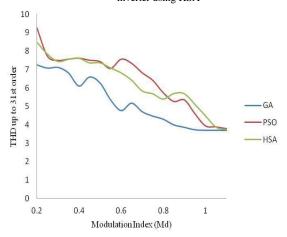


Fig.9 Voltage THD versus modulation index for 7-level multilevel inverter using GA, PSO and HSA considering lowest THD

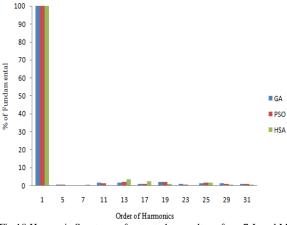


Fig.10 Harmonic Spectrum of output phase voltage for a 7-Level Multilevel inverter at 1 Md up to 31st order having THD 3.6854%, 3.8423% and 4.4878% using GA, PSO and HSA respectively

The results show that proposed approach for the harmonic optimization of multilevel inverters works properly. This method was applied to the seven and eleven levels cascaded multilevel inverters. Then, this method can be extended to more levels inverters. The proposed method is also capable of finding all possible sets of solutions of nonlinear equations. The results show that GA gives better performance in THD minimization and takes less computational time than PSO and HSA. In GA Toolbox all GA codes are in-built. So it is very easy to use GA Toolbox. However, in case of PSO and HSA the algorithm has to be developed either in MATLAB or other higher level languages.

# VI. CONCLUSION

Artificial intelligence especially Genetic Algorithm, Particle Swarm Optimization (PSO) and Harmony Search Algorithm are used for THD minimization in Seven Level Cascaded H-Bridge Multilevel Inverter. Selected lower order of harmonics are controlled within allowable limits while the fundamental output voltage is maintained at desired level, thus resulting in the minimum THD and the corresponding switching angles are computed. From the presented case studies it is observed that GA technique provides superior performance compared to PSO and HSA as far as the minimization of THD is concerned.

# VII. REFERENCES

[1] Bp "Statistical Review Of World Energy", June 2007

[2] Energy Outlook 2030.

[3] Schminke, "High power pulse step modulator for 500kwshort wave and 600 kw medium wave transmitters," brown bovery rev., *vol.* 72, no. 5, pp. 235–240, and 1985.

[4] M. Marchsoni, m.Mazzucchelli, and s.tenconi, "a nonconventional power converter for plasma stabilization," IEEE trans. power electron. vol. 5, pp. 212–219, apr. 1990.

[5] P.w. Hammond, "a New approach to enhance power quality for medium voltage ac drives," IEEE *trans. ind.* applicant., vol. *33*, pp. 202–208, Jan./Feb. 1997.

[6] J.D. Ainsworth, M. Davies, P J. Fitz, k.e. Owen, and d. r. trainer, "static var compensator (statcom) based on single phase chain circuit converters," *IEEE proc.—genre.* Transom. distrib., vol. 145, no. 4, July 1998.

[7] José rodriguez, jih-sheng lai, and f. z. peng, "Multilevel converters: a survey of topologies, controls and applications," IEEE trans. on ind. electronics, vol. 49, no. 4, pp. 724-738, august 2002.

[8] Pan z, peng fz (2006) harmonics optimization of the voltage balancing control for multilevel converter/inverter systems. IEEE Tran power electron 21:211–218.

[9] N. Mohan, t. m. undeland, and w. p. robbins, power electronics: converters, applications, *and* design, 2nd ed. new york: wiley, 1995.

[10] S. Jeevananthan, "evolutionary computing based area integration pwm technique for multilevel inverters", *j.* electrical systems 3-2 (2007): 61-72

[11] N. Yousefpoor, n. farokhnia, s. h. fathi, j. s. moghani, "developed single-phase omthd technique for cascaded multi-level inverter by considering adjustable dc sources," *in* proc. epecs, pp. 1-6, Nov. 2009.

[12] N.Y.ousefpoor, s. h. fathi, n. farokhnia, and h. a. abyaneh, "application of omthd on the line voltage of cascaded multi-level inverters with adjustable dc sources," *in* proc. iciea, pp. 498-503, jun. 2010.

[13] Patel hs, hoft rg., "Generalized techniques of harmonic elimination and voltage control in thyristor inverters: part i – harmonic elimination", IEEE trans ind appl 1973;3(3):310–7.

[14] Jasdish kumar, biswarup das, pramod agarwal, "selective harmonic elimination technique for a multilevel inverter", fifteenth national power systems conference (npsc), it Bombay, December 2008.

[15] T J Liang, r g hoft, "Walsh function method of harmonic elimination", proceedings eighth annual applied power electronics conference and exposition (1993),pp. 847-853.

[16] Razaaghi m, nazarzadeh j, nikravesh ky (1998) a block-pulse domain technique of harmonics elimination in multilevel pulse width modulated inverters. electr power syst res 46:77–81.

[17] Kennedy, j., & eberhart, r. (1995), "particle swarm optimization", IEEE international conference on neural networks, perth, Australia (vol. iv, pp. 1942–1948).

[18] M. J. schutten, d. a.torrey, "Genetic algorithms for control of power converters", conference proceedings on IEEE pesc, 1955, pp. 1321-1326. chiasson, j. n tolbert, l. m., mckenzie, k. j., & du, z. (2004).

[19] Geem z.w., kim j.h. and loganathan g.v. (2001) "a New heuristic optimization algorithm: harmony search" simulation, vol.76:2, pp 60-68.

[20] I. kougias and n. theodosiou, a new music-inspired harmony based optimization algorithm. theory and applications, international conference on protection and restoration of the environment x, corfu, 2010.

[21] A.I maswood, s. wei, and m.a. rahman,"A flexible way to generate pwm-she switching patterns using genetic algorithm", proc. of IEEE applied power electronics conference (apec'01), pp. 1130-1134, 2001

[22] Sumandebnath, rup Narayen roy, harmonic elimination in multilevel inverter using and pso comparison, IEEE students' conference on electrical, electronics and computer science (sceecs 2012), doi:10.1109/sceecs.2012.6184789, isbn: 978-1-4673-1516-6.

[23] Rupnarayan ray, debashis chatterjee, swapan kumar goswami ,"an application of pso technique for harmonic elimination in pwm inverter", *applied soft computing*. vol-9, issue 4 (sept'2009), pp.1315-1320.

[24] R.N Ray, d. chatterjee, s.k. goswami, "Harmonics elimination in a multilevel inverter using the particle swarm optimisation technique", iet power electron., 2009, vol. 2, issue. 6, pp. 646–652

[25] Suman debnath, dr. rup Narayen roy, "the optimization in 13 level photovoltaic inverter using genetic algorithm", international journal of engineering research and applications , vol. 2, issue 3, may-june 2012, pp. 385-389